SURFACE MOUNT SCHOTTKY BARRIER DIODE ARRAY
SPICE MODELS: SD103ASDM

## Features

- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- Fast Switching
- Low Reverse Capacitance
- Also Available in Lead Free Version


## Mechanical Data

- Case: SOT-26, Plastic
- Case material - UL Flammability Rating Classification 94V-0
- Moisture sensitivity: Level 1 per J-STD-020A
- Polarity: See Diagram
- Leads: Solderable per MIL-STD-202, Method 208
- Also Available in Lead Free Plating (Matte Tin Finish). Please see Ordering Information, Note 4, on Page 3


| SOT-26 |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Dim | Min | Max | Typ |  |
| A | 0.35 | 0.50 | 0.38 |  |
| B | 1.50 | 1.70 | 1.60 |  |
| C | 2.70 | 3.00 | 2.80 |  |
| D | - | - | 0.95 |  |
| F | - | - | 0.55 |  |
| H | 2.90 | 3.10 | 3.00 |  |
| J | 0.013 | 0.10 | 0.05 |  |
| K | 1.00 | 1.30 | 1.10 |  |
| L | 0.35 | 0.55 | 0.40 |  |
| M | 0.10 | 0.20 | 0.15 |  |
| $\alpha$ | $0^{\circ}$ | $8^{\circ}$ | - |  |
| All Dimensions in | $\mathbf{m m}$ |  |  |  |
|  |  |  |  |  |

- Marking: KSU (See Page 3)
- Weight: 0.016 grams (approx.)


## Maximum Ratings @ $T_{A}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Peak Repetitive Reverse Voltage <br> Working Peak Reverse Voltage <br> DC Blocking Voltage | $\mathrm{V}_{\mathrm{RRM}}$ <br> $\mathrm{V}_{\mathrm{RWM}}$ | 40 | V |
| RMS Reverse Voltage | $\mathrm{V}_{\mathrm{R}(\mathrm{RMS})}$ | 28 | V |
| Forward Continuous Current (Note 1) | $\mathrm{I}_{\mathrm{FM}}$ | 350 | mA |
| Non-Repetitive Peak Forward Surge Current @ $\mathrm{t} \leq 1.0 \mathrm{~s}$ | $\mathrm{I}_{\mathrm{FSM}}$ | 1.5 | A |
| Power Dissipation (Note 1) | $\mathrm{P}_{\mathrm{d}}$ | 225 | mW |
| Thermal Resistance, Junction to Ambient Air (Note 1) | $\mathrm{R}_{\theta J \mathrm{AA}}$ | 444 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating and Storage Temperature Range | $\mathrm{T}_{\mathrm{j}}, \mathrm{T}_{\text {STG }}$ | -65 to +125 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse Breakdown Voltage (Note 2) | $\mathrm{V}_{(\mathrm{BR}) \mathrm{R}}$ | 40 | - | - | V | $\mathrm{I}_{\mathrm{R}}=100 \mu \mathrm{~A}$ |
| Forward Voltage Drop (Note 2) | $V_{\text {F }}$ | - | - | $\begin{aligned} & 0.37 \\ & 0.50 \\ & 0.60 \end{aligned}$ | V | $\begin{aligned} & I_{F}=20 \mathrm{~mA} \\ & I_{F}=100 \mathrm{~mA} \\ & I_{F}=200 \mathrm{~mA} \end{aligned}$ |
| Reverse Current (Note 2) | IR | - | - | 5.0 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{R}}=30 \mathrm{~V}$ |
| Total Capacitance | $\mathrm{C}_{\top}$ | - | 50 | - | pF | $\mathrm{V}_{\mathrm{R}}=0 \mathrm{~V}, \mathrm{f}=1.0 \mathrm{MHz}$ |
| Reverse Recovery Time | $\mathrm{trr}_{\text {r }}$ | - | 10 | - | ns | $\begin{aligned} & I_{F}=I_{R}=200 \mathrm{~mA}, \\ & I_{r r}=0.1 \times I_{R}, R_{L}=100 \Omega \end{aligned}$ |

Notes: 1. Device mounted on FR-5 PCB $1.0 \times 0.75 \times 0.062$ inch pad layout as shown on Diodes Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
2. Short duration test pulse used to minimize self-heating effect.

$\mathrm{V}_{\mathrm{F}}$, INSTANTANEOUS FORWARD VOLTAGE (mV)
Fig. 1 Typical Forward Characteristics


Fig. 2 Typical Reverse Characteristics


Fig. 3 Typ. Total Capacitance vs. Reverse Voltage
Fig. 4 Power Derating Curve

Ordering Information (Note 3)

| Device | Packaging | Shipping |
| :---: | :---: | :---: |
| SD103ASDM-7 | SOT-26 | $3000 /$ Tape \& Reel |

Notes: 3. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.
4. For Lead Free version (with Lead Free terminal finish) part number, please add "-F" suffix to part number above. Example: SD103ASDM-7-F.

## Marking Information



